

091623952

APPLICATION NO.	CONT/PRIOR	CLASS <i>375</i>	SUBCLASS <i>223</i>	ART UNIT <i>2634</i>	EXAMINER <i>DEPPE</i>
09/623952	D F	<i>700 744</i>	<i>740</i>	<i>2121 2131</i>	<i>Decmch</i>

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E Method and apparatus for clock timing recovery in chidsl, particularly vdsl modems

PTO-2040
12/99

ISSUING CLASSIFICATION

TERMINAL DISCLAIMER	DRAWINGS			CLAIMS ALLOWED	
	Sheets Drwg.	Figs. Drwg.	Print Fig.	Total Claims	Print Claim for O.G.
<input type="checkbox"/> The term of this patent subsequent to _____ (date) has been disclaimed.				NOTICE OF ALLOWANCE MAILED	
	(Assistant Examiner) _____ (Date)				
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	(Primary Examiner) _____ (Date)			Amount Due	Date Paid
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